

Notice of References Cited	Application/Control No.	Applicant(s)/Patent Under Reexamination	
	09/752,880	HAYCOCK ET AL.	
	Examiner	Art Unit	Page 1 of 1
Thomas J. Cleary	2111		

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,801,549	09-1998	Cao et al.	326/83
*	B	US-6,704,277	03-2004	Dabral et al.	370/201
*	C	US-5,604,450	02-1997	Borkar et al.	326/82
*	D	US-6,128,754	10-2000	Graeve et al.	714/39
*	E	US-6,661,303	12-2003	Ghoshal, Uttam Shyamalindu	333/1
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Mooney, Randy, et al., "A 900 Mb/s Bidirectional Signalling Scheme". IEEE Journal of Solid-State Circuits. Vol. 30, No. 12. December 1995. pp. 1538-1543.
	V	"The Free On-Line Dictionary of Computing". [entry: hardware]. Online 23-January 1997. Retrieved 31-March 2006. < http://foldoc.org/foldoc/foldoc.cgi?query=hardware >.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.